

#### **ELECTRIC DOUUBLE LAYER CAPACITORS**

### PRODUCT SPECIFICATION

### 規格書

**CUSTOMER:** DATE:

(客戶): 志盛翔 (日期): 2018-08-09

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DDL 5.5V1F (φ9x17.5x19.5)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER		
PREPARED (拟定)	CHECKED (审核)	
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CUSTOMER		
SIGNATURE		
(签名)		

SAMXON ELECTRONIC
COMPONENTS LIMITED

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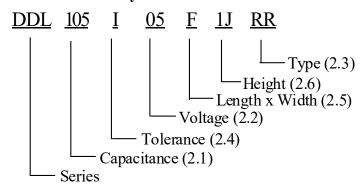
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## ELECTRIC DOUBLE LAYER CAPACITORS SPECIFICATION DDL SERIES

#### 1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

#### 2. Part Number System



2.1 <u>Capacitance code</u>

Code	105
Capacitance (F)	1

2.2 Rated voltage code

tarea remage code	
Code	05
Voltage (W.V.)	5.5

2.3 <u>Type</u>

Code	RR
Type	Bulk

#### 2.4 <u>Capacitance tolerance</u>

"I" stands for  $0\% \sim +50\%$ 

2.5 Length x Width

Code	F
Length x Width	9x17.5

#### 2.6 Height

1J=19.5mm

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#### 3. Characteristics

#### Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C
Relative humidity : 25% to 75%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature:  $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

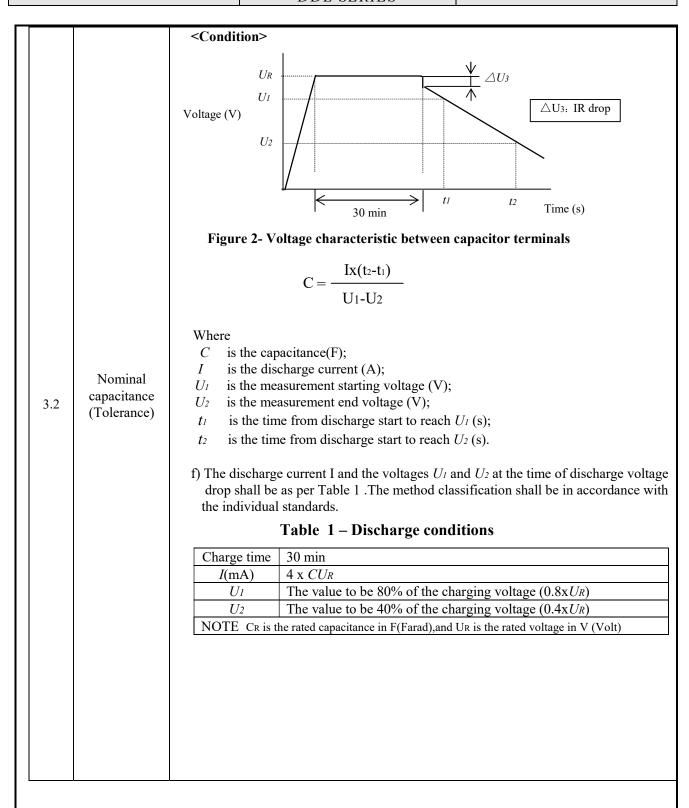
#### Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 70°C.

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ITEM	PERFORMANCE
3.1 Rated vo (WV) Surge vo (SV)	WV (V.DC) 5.5 SV (V.DC) 5.7
3.2 Nomir capacita (Tolerar	ce S changeover switch Cy capacitor under test

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3.3	ESR	<pre><condition> Measuring frequency :1kHz Measuring temperature:20±2°C Measuring point : 2mm max from the surface of a sealing resin on the lead wire. </condition></pre> <criteria> (20°C)Less than the initial limit: ESR≤1.05Ω</criteria>								
3.4	Leakage current	1. An 2.The 3. De <crit Less t I≤0.09</crit 	<condition> 1. Ambient temperature: 25°C ± 2°C. 2. The electrification time:72H 3. Desistance value of protective resistor less than 1Ω.  <criteria> Less than the initial limit(25°C ± 2°C): I≤0.09mA I is the Leakage current</criteria></condition>							
		<cond< td=""><td colspan="7"><pre><condition> STEP    Temperature(°C)</condition></pre></td></cond<>	<pre><condition> STEP    Temperature(°C)</condition></pre>							
		1	20±2	Capacitance, ESR						
					△ C/C	Within ±30% of initial capacitance				
		2	-40+3	ESR	Less than or equal to 4 times of the value of item 3.3					
3.5	Temperature	3	Keep at 15 to 35°C for 15 minutes or more							
	characteristic	4	70±2	△ C/C	Within ±30% of initial capacitance					
			7012	ESR	The limit specified in 3.3					
			a. ESR -40°C/ ESR 20°C: ESR ratio at 1kHz; b. ΔC/C 20°C: Capacitance change;							

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		<criteria>  Item</criteria>	Performance
		Capacitance Change	Within ±30% of initial capacitance
2.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
3.6	test	Appearance	No visible damage and no leakage of electrolyte
			exposed for 240±48 hours in an atmosphere of 90~95%R
		<pre>40±2°C, the characteris</pre>	stic change shall meet the following requirement.
		Item	Performance
	Damp	Capacitance Change	Within ±30% of initial capacitance
3.7	heat test	ESR	Less than or equal to 4 times of the value of item 3.3  No visible damage and no leakage of electrolyte
		Appearance	

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		a) Lead pull strength A static load force shall be ap in a direction away from the		rminal in the axial direction and acting
		Lead wire diameter		Load force (N)
		d ≤0.5	,	5
3.8	Lead strength	table above is applied to one le horizontal position and then re for 2~3 seconds.  The additional bends are made  Lead wire diameter (n  d ≤0.5  Performance: The characterist  Item  Capacitance Change	ead and then to turned to a vere in the opposition shall meet to the Performance Within ±30%	Load force (N)  2.5 the following value after a) or b) test.
		Appearance	leakage of el	
3.9	Resistance to vibration	Performance: Capacitance value she capacitance when the value is mean	ig2 nall not show of sured within 3	

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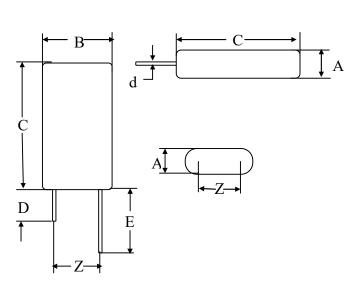
3.10	Solderability	The capacitor shall be tested under the following conditions:  Solder : Sn-3Ag-0.5Cu  Soldering temperature: 245±3°C  Immersing time : 2.0±0.5s  Immersing depth : 1.5~ 2.0mm from the root.  Flux : Approx .25% rosin  Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method  Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylachol solution of colophony.  Then it will be immersed at the surface of the solder with the following condition:  Solder : Sn-3Ag-0.5Cu  Soldering temperature : 260 ±5°C  Immersing time : 5±0.5s  Heat protector: t=1.6mm glass -epoxy board  B) Soldering iron method  Bit temperature : 350 ±10°C  Application time : 3.5 ±0.5 s  Heat protector: t=1.6mm glass -epoxy board  For both methods, after the capacitor at thermal stability, the following items shall be measured:  Item Performance  Capacitance Change Within ±10% of initial capacitance  No visible damage legible marking and no leakage of electrolyte

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# ELECTRIC DOUBLE LAYER CAPACITORS SPECIFICATION DDL SERIES

Unit: mm

#### 4. Product Dimensions



Note: Longer lead is positive

A	В	C	d	D	Е	Z
max.	max.	max.	±0.05	min	min	±0.50
9.0	17.5	19.5	0.50	15.0	19.0	11.8

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_	<b>T</b> T	. •	• .
5	No	tice	item

- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction  $\Delta V=IR$  will happen at the moment of discharge.
- ( 6 ) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within -30°C~50°C temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- ( 10 ) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)
- ( 11 ) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

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